

Computer Architecture Lab's Book Citings through 2017: Group Three

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Abstract — Citations are summarized from books. The citing book is determined and the cited articles are listed. These are indexed for more rapid identification within electronically-formatted and printed books. The citation may be followed to view the cited articles. Only books which are not proceedings volumes have been included within this document.

Keywords — *Book citations, Citing article, Citation index, Cited articles.*

1.0 Introduction

In this paper, the objective is to identify book citations for future retrieval. Searches included “R.F. DeMara” and “DeMara, R” as well as “R DeMara” which were then inspected manually for matching content. The paper provides a concise list of them that would not otherwise be available in a single document. Sources listed were obtained via web search and then filtered as indicated below.

2.0 Book Citings as Indexed within Google Books

The book: Acton, Q. Ashton, ed. Advances in Machine Learning Research and Application. Scholarly Editions, 2012, cited [1] on page 1086.

The book Gottschalk, Petter. Policing Financial Crime: Intelligence Strategy Implementation. Universal-Publishers, 2009, cited [2] on page 280.

The book Megson, Graham M., and Xian Chen. Automatic parallelization for a class of regular computations. World Scientific, 1997, cited [3] on page 170.

The dissertation [11] Roberts, James D. Doctor of Philosophy in Computer Engineering. Diss. University of California Santa Cruz, 1995, cited by [4] according to google scholar.

3.0 Conclusion

Based on the citations above, it is possible to rapidly locate the articles by google scholar search using the stated booked titles. Citations appear on the pages as mentioned. The cited articles are listed as indicated.

References

- [1] R. F. DeMara, K. Zhang, and C. A. Sharma “Autonomic Fault-Handling and Refurbishment Using Throughput-Driven Assessment,” *Applied Soft Computing*, Volume 11, Issue 2, March 2011, pp. 1588 – 1599.
- [2] R. C. Watkins, K. M. Reynolds, R. F. DeMara, M. Georgiopoulos, A. J. Gonzalez, and R. Eaglin, “Tracking dirty proceeds: Exploring data mining technologies as tools to investigate money laundering,” *Journal of Policing Practice and Research: An International Journal*, Vol. 4, No. 2, January, 2003, pp. 163 – 178.
- [3] R. F. DeMara and D. I. Moldovan, “The SNAP-1 Parallel AI Prototype,” *IEEE Transactions on Parallel and Distributed Systems*, Vol. 4, No. 8, August, 1993, pp. 841 – 854.
- [4] R. F. DeMara, “Parallelism, design, and performance of a marker-propagation reasoning system,” Ph.D. Dissertation, University of Southern California, 1992.

Computer Architecture Lab/SS2017. From Wikiversity. < Computer Architecture Lab. Jump to navigation Jump to search. Contents. 1 Group 1. 1.1 SDRAM controller for Patmos in Chisel. 1.2 Source Access. 2 Group 2. 3 Group 3. 4 Group 4. 5 Group 6.Â Project Topic: Branch Predictor for Patmos. Three things must be predicated in order to implement a branch predictor: Whether the fetched instruction is a branch. (Conditional) Branch Direction. Branch Target Address. The first and the third issue can be resolved by implementing a Branch Target Buffer (BTB). The second issue can be resolved either by static or dynamic analysis. Read book Learning Computer Architecture with Raspberry Pi EPUB. Total Inventor's Manual: Transform Your Idea into a Top-Selling Product (Popular Science) Sean Michael Ragan (Author). Learning Computer Architecture with Raspberry Pi (eBook).Â Digital Design and Computer Architecture takes a unique and modern approach to digital design. Beginning with digital logic gates and progressing to the design of combinational and sequentialâ€¦ read more at Kobo.Â The layering of the laser-cut elements creates depth to slowly unveil Eliasson's home at a three dimensional perspective. Berlin based design studio founded by Michael Heimann and Hendrik Schwantes.